



IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re U.S. Patent Application of)
SHIMAMOTO *et al.*)
Application Number: 10/020,314)
Filed: December 18, 2001)
For: FABRICATING METHOD OF)
SEMICONDUCTOR INTEGRATED CIRCUITS)
Attorney Docket No. HITA.0143)

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TECHNOLOGY CENTER 2850
Art unit: 2823
Examiner:
Nguyen, Khiem D.

Honorable Assistant Commissioner
for Patents
Washington, D.C. 20231

RESPONSE AND AMENDMENT UNDER 37 C.F.R. §1.111

Sir:

This is in response to the office action dated May 6, 2002, in the above identified application, the period for response is set to expire on August 6, 2003. Please amend the above identified application with the replacement paragraphs/sections/claims as follows: